



# DELHI TECHNOLOGICAL UNIVERSITY

Established by Govt. of Delhi vide Act 6 of 2009  
(FORMERLY DELHI COLLEGE OF ENGINEERING)  
BAWANA ROAD, DELHI-110042

Advt. No. DTU-MeitY-MCEV/EED/03

10<sup>th</sup> November 2020

## ADVERTISEMENT TO FILL UP PROJECT POSITIONS

A walk-in-interview for filling of project positions as per the details given below for the MeitY Sponsored R&D Project under Chief Investigator – Prof. Mini Sreejeth, Electrical Engineering Department (EED), Delhi Technological University (DTU) and Co-investigator – Prof. Indu S, Department of Electronics and Communication Engineering, DTU is scheduled to be held on **23<sup>rd</sup> November 2020** in the Conference Hall of the EED.

- Title of the Project** : Development and deployment of motor controller for low to medium Power Electric Vehicles
- Sponsoring Agency** : Ministry of Electronics and Information Technology (MeitY)

### 3. Details of Project Positions

Sl. No	Désignation	Minimum Qualification	Experience	Nos.	Monthly Emolu-ments (₹)
a)	Engineer	B.E. / B. Tech. in Electrical Engineering / Electrical and Electronics Engineering / Electronics and Communication Engineering <b>OR</b> M.E. / M. Tech. (Power Electronics / Electric Drives)	At least 5 years industrial experience in design and development of power electronics and electronics hardware design as per details given in Job Description	1	80,000 (consolidated)
b)	SRF	a) B.E. / B. Tech. in Electrical Engineering / Electrical and Electronics Engineering / Electronics and Communication Engineering <b>AND</b> M.E. / M. Tech. (preferably in Power Electronics) b) 1 <sup>st</sup> Division at B.E./ M.E. levels <b>Desirable:</b> NET/ GATE Qualified	At least 1 year experience in power electronics and electronics hardware design as per details given in Job Description	1	28,000 + 30% HRA
c)	Technician	Diploma in Electrical Engineering/ Electrical and Electronics Engineering Electronics and Communication Engineering	At least 2 years industrial experience as per details given in Job Description	1	17,924 (consolidated)

- Age Limit** : 55 years for Engineer and 35 years for SRF and Technician. Age relaxation for candidates belonging to SC/ST/PH/OBC categories as per government norms.
- Duration** : Appointment to the above post shall **initially** be for a period of three months or for the duration of the project, whichever is earlier.

*Mini Sreejeth*  
(Prof. Mini Sreejeth)  
Chief Investigator  
MeitY Sponsored R&D Project  
EED, DTU

*Indu Ashok*  
(Prof. Indu S)  
Co-Chief Investigator  
MeitY Sponsored R&D Project  
ECED, DTU

-Sd-  
(Prof. Uma Nangia)

6. **Job description**

a)	Engineer	:	<ul style="list-style-type: none"> <li>• Embedded software development on 32 bits ST Microelectronics microcontroller platform</li> <li>• Software debugging and project documentation.</li> <li>• Power electronics and electronics hardware design</li> <li>• Must have experience on RS232, RS485 and CAN bus communication</li> <li>• Excellent team work</li> </ul>
b)	SRF		<ul style="list-style-type: none"> <li>• Embedded software development on 32 bits ST Microelectronics microcontroller platform</li> <li>• Software debugging and project documentation.</li> <li>• Power electronics and electronics hardware design</li> </ul>
c)	Technician	:	<ul style="list-style-type: none"> <li>• SMD mounting and soldering on multi-layered PCB</li> <li>• On board testing</li> <li>• Wiring</li> </ul>
<p>• <b>Note:</b> The completion of the research project being time-bound, preference will be given to candidates who are willing to join the Project with immediate effect and willing to work beyond the call of normal duty.</p>			

7. Candidates before appearing for the Walk in Interview shall ensure that they are eligible for the position they intend to apply.
8. Candidates applying for the above post are requested to report on **23<sup>rd</sup> November 2020 at 10:30 am** for registration, verification of documents, test / interview at the **Department of Electrical Engineering, DTU**. Candidates reporting **after 11:00 am will not be entertained**.
9. Candidates desirous to appear for the Walk in Interview should bring a set of self-attested copies of all certificates, testimonials, the latest CV and a passport sized colour photograph along with the original documents. The Application should include: -
  - Details of degree/certificates obtained starting from Class X (or equivalent) clearly indicating the aggregate percentage / CGPA
  - Experience in research, industrial field, academics and others.
10. Candidate shall bring along with them the original degree(s)/certificate(s) and experience certificate(s) at the time of walk in interview for verification.
11. Please note that no TA/DA is admissible for attending the walk-in interview.
12. The interview will be held in **Conference Hall of Department of Electrical Engineering, DTU on 23<sup>rd</sup> November 2020 at 11:00 am**.
13. The competent authority reserves the right to reject the candidature of any or all of the candidates without assigning any reason thereof.

(Prof. Mini Sreejeth)  
Chief Investigator  
MeitY Sponsored R&D Project  
EED, DTU

(Prof. Indu S)  
Co-Chief Investigator  
MeitY Sponsored R&D Project  
ECED, DTU

-Sd-  
(Prof. Uma Nangia)