

VDSemiX  
05/03/2026

**Vinod Dham Centre of Excellence for Semiconductors and Microelectronics (VDSemiX)**

Mid-Semester Date Sheet, March-2026

Date/Slot	08:30 AM to 10:00 AM	10:30 AM to 12:00 Noon	12:30 PM to 02:00 PM	02:30 PM to 04:00 PM	04:30 PM to 06:00 PM
09.03.2026 (Monday)	SC302 (Compact Modelling and Process Simulation of Semiconductor Devices) (Slot-E1) (SPS5)				
10.03.2026 (Tuesday)	EP318n (Advanced Chiplet Packaging & Reliability Engineering) (Slot-E5) (AB3-506, AB3-513, AB3-717, AB3-401)				

For any query, contact the teacher concerned.

RChaujar  
05/3/26  
(Prof. Rishu Chaujar)  
Director, VDSemiX, DTU

Copy to:

1. CoE, DTU for information.
2. All HOD's
3. Notice Board
4. Head, CC (with a request to upload the date-sheet on the university website)