ELECTRICAL ENGINEERING DEPARTMENT DELHI TECHNOLOGICAL UNIVERSITY

SHAHBAD DAULATPUR, BAWANA ROAD, DELHI-110042

No. F. DTU/EED/IMP.Generator/PQL/467/2023 1280

7th, November, 2023

NOTICE INVITING QUOTATIONS

With reference to the NIQ dated 3.10.2023, the due date has been extended till 15.11.2023 by 5 PM. The sealed quotations on company's letter head including GSTIN no. should reach to the office of the Head, Department of Electrical Engineering, Delhi Technological University, Delhi-110042.

S. No.	Items/Specification	Make	Qty
1	On site calibration of 300KV impulse voltage divider (make KVTEK) traceable to IEC 60060 part II with uncertainty less than 1% for voltage and less than 2% for rice and tail time.	Any	01
2	On site calibration of impulse analyzer (make KVTEK) traceable to IEC 61083 part I with uncertainty less than 1%	Any	01
2	On site calibration of 100 KV Power frequency transformer (Make RE) traceable to IEC 61083 part II with uncertainty less than 1%	Any	01

(Prof. Rachana Garg)

Head,

Department of Electrical Engineering

Professor & Head
Electrical Engineering Department
Delhi Technological University
(Formerly Delhi College of Engineering)
Bawana Road, Delhi-110042

Copy to

- 1. Sr. Account Officer
- 2. HOD (CC) to upload on DTU website
- 3. Notice board, EED.