



DELHI TECHNOLOGICAL UNIVERSITY

Established by Govt. of Delhi vide Act 6 of 2009

(Formerly Delhi College of Engineering)

SHAHBAD DAULATPUR, BAWANA ROAD, DELHI-110042

No. F.DTU/Rectt./AP/EE&ECE/2016

Dated: 12/11/2016

Schedule for Presentation

The schedule for presentation of the candidates who have been shortlisted on the basis of screening test held on 12.11.2016 for the post of Assistant Professor in the discipline of Electrical Engineering and Electrical & Electronics Engineering are as under:-

Date	Time	Category	Venue
16.11.2016	9.30 a.m.	SC candidates mentioned in the list of shortlisted candidates on the basis of screening test from S. No. 1 to 10.	Committee Room, Department of Electrical Engg. , DTU
	1.30 p.m.	SC candidates mentioned in the list of shortlisted candidates on the basis of screening test from S. No. 11 to 16. All OBC candidates mentioned in the list of shortlisted candidates on the basis of screening test.	
17.11.2016	9.30 a.m.	UR candidates mentioned in the list of shortlisted candidates on the basis of screening test from S. No. 1 to 15.	
	1.30 p.m.	UR candidates mentioned in the list of shortlisted candidates on the basis of screening test from S. No. 16 to 30.	

Further, the candidates are advised to select any topic of presentation from third year/final year subject of Undergraduate programme. The presentation should have only five slides.

Detailed schedule of Interview will be displayed on the University website within due course of time.

Sd/-
Asst. Registrar (Estt.)