

## ABOUT THE UNIVERISTY

Delhi Technological University (formerly Delhi College of Engineering) is a key node in national and global knowledge network, thus empowering India with the wings of knowledge and power of innovation. Established in 1941 by Government of India, erstwhile DCE now DTU is one of the premier Institution of Engineering and Technology education in India. It has played unique and important roles in the advancement of Technical education in the country.

## ABOUT THE DEPARTMENT

The Department of Electronics and Communication Engineering has seen considerable growth since its inception in 1976. The vision of the department is to focus on the incubation of innovations in the areas of electronic design/ fabrication, and communication technologies, which are needed to address the growing challenges of tomorrow. This department offers UG, PG and Ph. D. programmes. Currently the Department has 11 well equipped curriculum laboratories, 4 research laboratories and separate departmental project laboratories. Faculty members of the department have been regularly contributing towards International and National Journals of repute from publishers like IEEE Transactions, IET, Wiley, Springer and Elsevier, etc. along with Proceedings of National and International Conferences. The Department regularly organizes seminars, workshops and training programs to keep pace with the new developments and recent trends in relevant technologies.

**Chief Patron**  
**Prof. Yogesh Singh,**  
Vice Chancellor

**Patron**  
**Prof. S.K. Garg,**  
Pro-Vice Chancellor - I

**Chairman**  
**Prof. S. Indu,**  
HOD, Electronics and Communication  
Engineering Department

**Course Coordinators**  
**Dr. N Jayanthi,**  
Assistant Professor, Electronics and  
Communication Engineering Department, DTU

**Ms. Trasha Gupta**  
Assistant Professor, Department of Applied  
Mathematics, DTU

**Venue**  
Seminar Hall,

## IMPORTANT DATES

Last date of Registration: 15<sup>th</sup> July 2019  
Intimation of Confirmation: 18<sup>th</sup> July 2019

## FOR ANY QUERIES, CONTACT

Dr. N. Jayanthi – 9868798801,  
Ms. Trasha Gupta – 9811978055

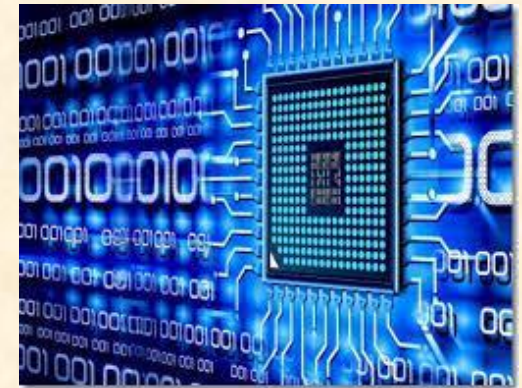
## TEQIP III SPONSORED

## ONE WEEK STAFF DEVELOPMENT PROGRAM

ON

## ELECTRONIC DESIGN AUTOMATION AND DOCUMENTATION TOOLS

July 22 – 26, 2019



Organized by



DEPARTMENT OF ELECTRONICS AND  
COMMUNICATION ENGINEERING  
DELHI TECHNOLOGICAL UNIVERSITY  
BAWANA ROAD,  
SHAHBAD, DAULATPUR,  
NEW DELHI—110042, INDIA

## ABOUT THE PROGRAM

TEQIP III sponsored one-week staff development program on “Electronic Design Automation and Documentation Tools” aims to equip the technical staff and students with latest technologies and tools for automating electronic designs and its documentation. EDA is a category concerning the algorithms and tools for designing integrated circuits via programming.

This program will help the technical staff to improve their skills in handling the laboratory equipment and documentation using related software. Our focus is on giving training on programming software like Python and the design of analog and digital circuits using industry standard software. The discussion on the various software platforms will be done by experts from the reputed institutes and industry.

## OBJECTIVES OF THE PROGRAM

1. The main purpose of training is to ensure that all the staff members have the technical skills needed to perform the experiments efficiently and smoothly.
2. To Impart the knowledge, education and tools to work better.
3. This creates, engagement and enthusiasm among staff that boost performance and benefit the University.
4. This SDP is planned to give hands on training to enhance the programming skills of the participating staff members.

## PROGRAM CONTENT

The content includes theory and practical aspects of following topics

- EDA tools
- Latex programming
- Python Programming
- MATLAB Programming
- Cadence/Mentor Graphics /VLSI tool
- Simulation Software

## PRICIPATION

The program is open to the faculty members, staff members of AICTE/UGC approved Institutes / Universities / R&D Lab and research scholars and P.G. students, who wish to enhance their technical skills in the field of Electronic Design Automation and Documentation Tools. The interested candidates should mail the scanned copy of duly filled application form approved by competent authority on/before 15<sup>th</sup> July, 2019 to [dtu.sdp.edadt@gmail.com](mailto:dtu.sdp.edadt@gmail.com). Confirmation of participants would be intimated though email by 18<sup>th</sup> July, 2019. Also submit the form in original at the time of registration for SDP on 22<sup>nd</sup> July 2019.

**There is no registration fees and no TA/DA will be paid to the participants.**

## REGISTRATION FORM TEQIP III Sponsored One Week Staff Development Program

On

### Electronic Design Automation and Documentation Tools

Organized by  
**ECE DEPARTMENT, DTU**  
**July 22 – 26, 2019**

Name: .....

Designation: .....

Organization: .....

Professional Field: .....

Mailing Address: .....

.....

E-mail Address: .....

Mobile No. ....

Date Signature of the applicant

**Note:** The application is hereby should be recommended and applicant is permitted to attend the workshop. Signature of the Head of the Department / Organization

**Signature and Seal of Head of Institution**